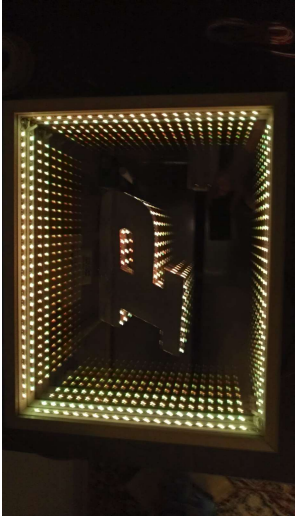


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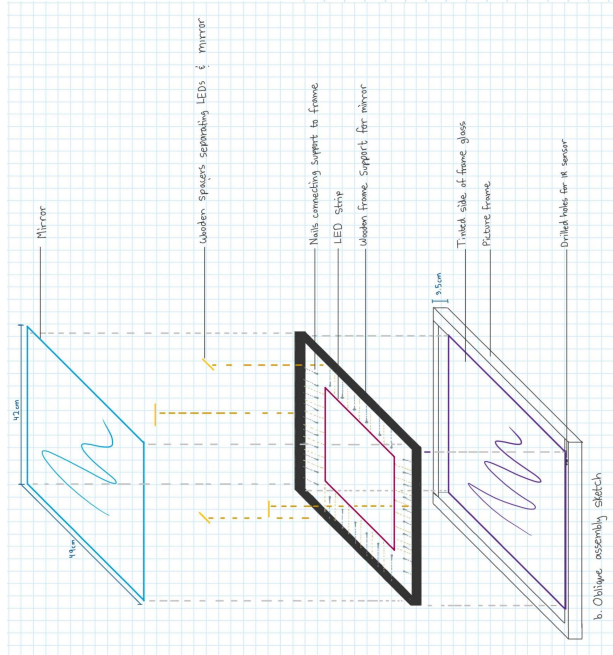
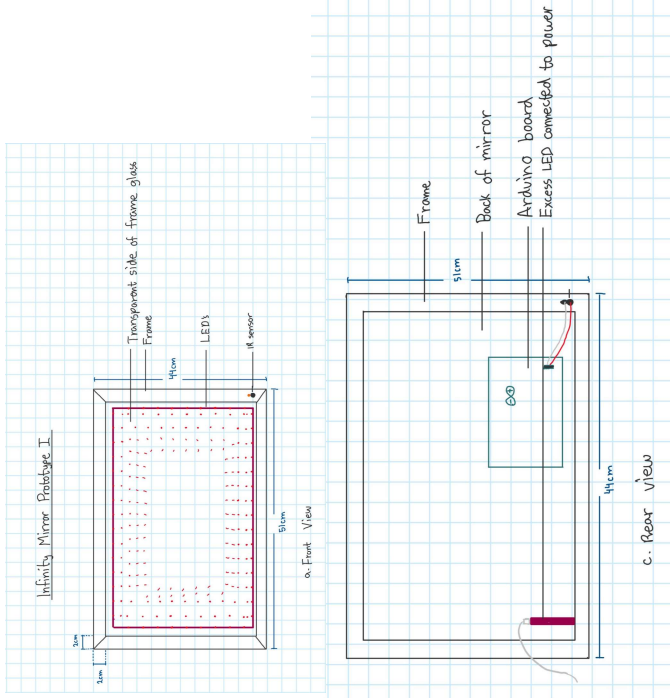
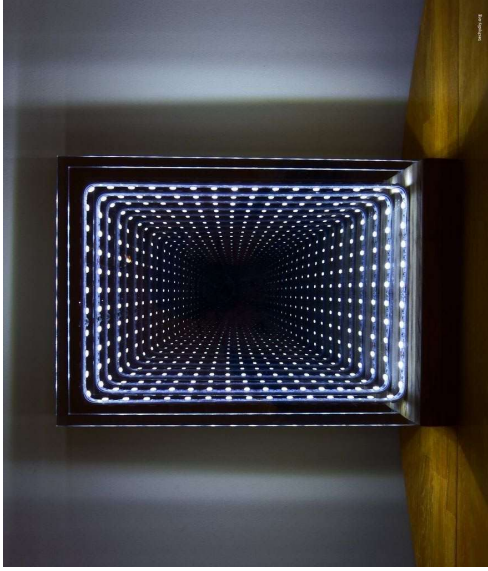
# Equilibrium Design Presentation

By: David, Josh, Mohammad, Jeff, and Harsh

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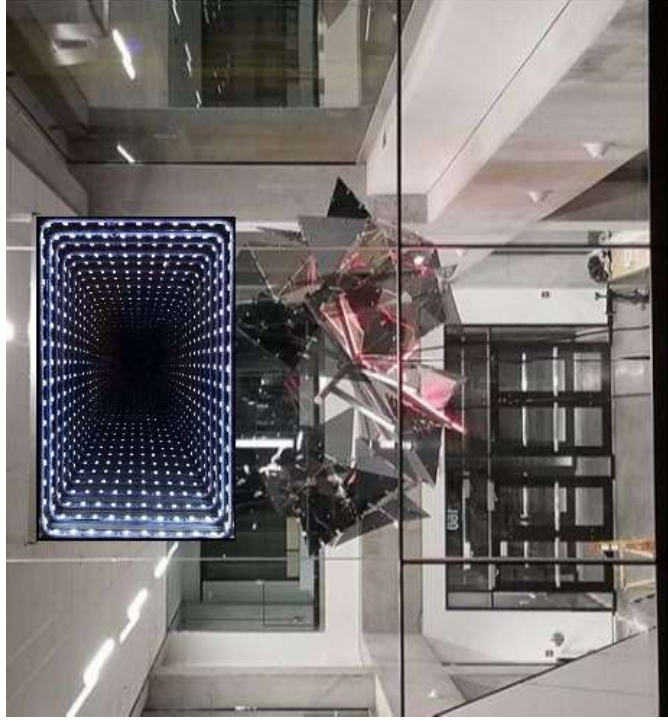
# What is our design



An Infinity mirror

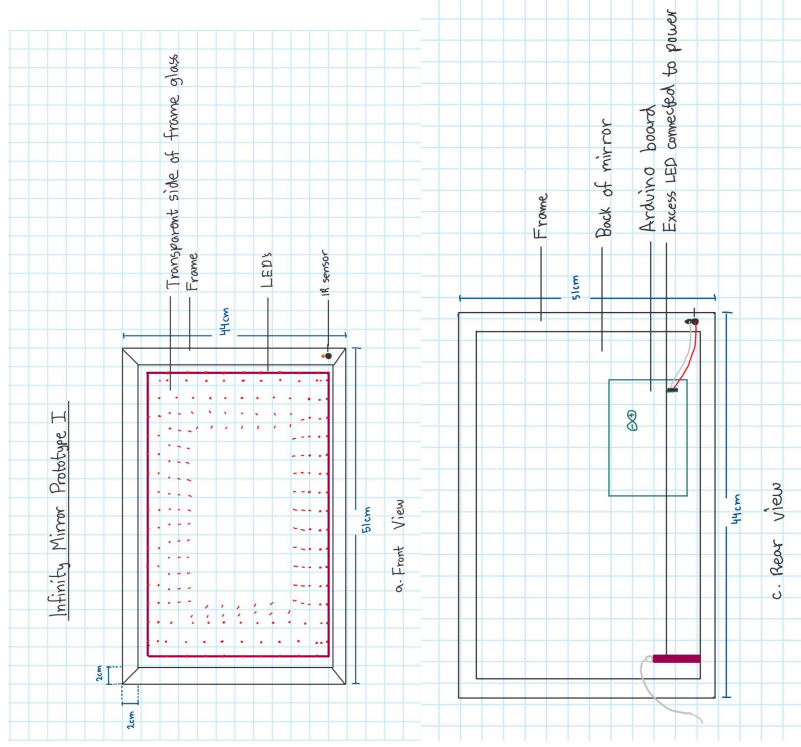
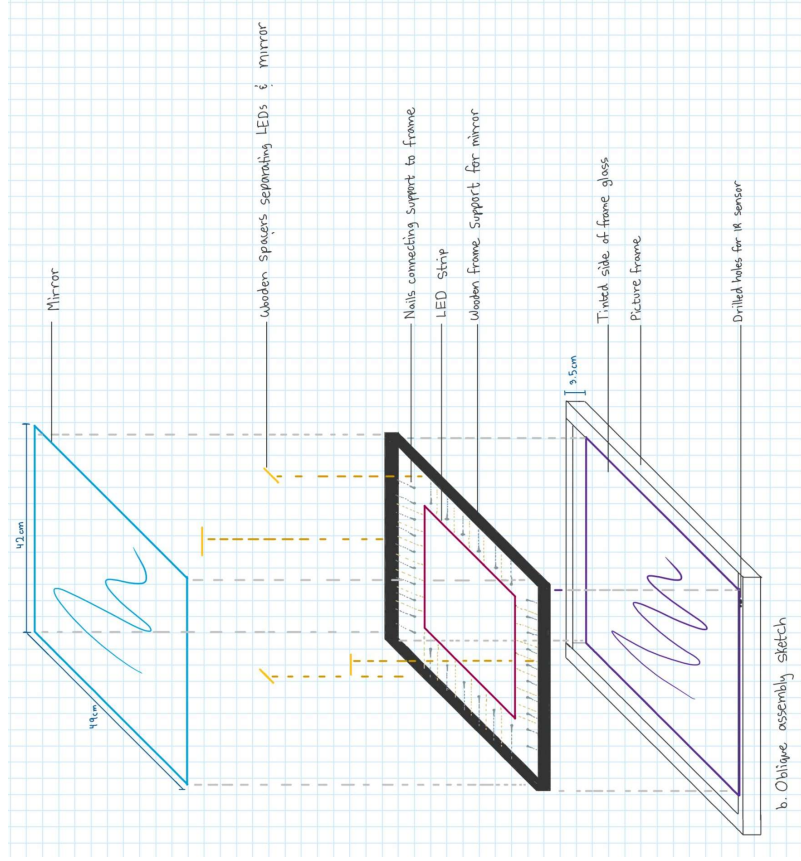
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# How to incorporate

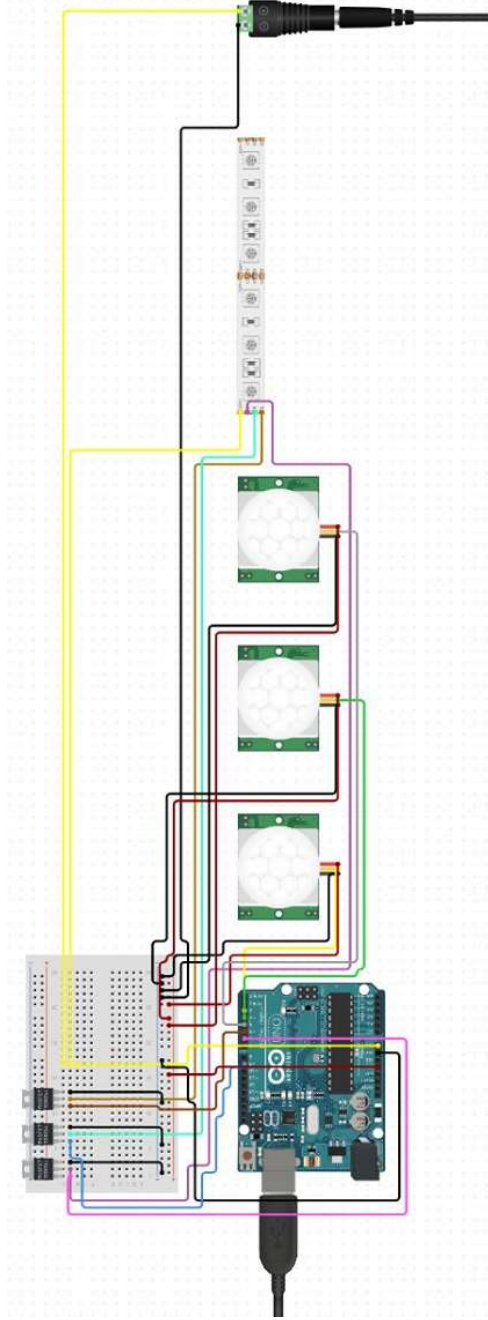


- Infinity mirror frame
  - IR sensor implementation
-

# Prototype I



# Prototype II



```
// Includes "libusb.h"
#include <libusb.h>
// Includes "usb.h"
#include <usb.h>

// Pin declarations
#define LED1_PIN 18 // Pin 18
#define LED2_PIN 23 // Pin 23
#define LED3_PIN 24 // Pin 24
#define SW_PIN 17 // Pin 17
#define SW_PULLUP_PIN 18 // Pin 18

// Switch pin declarations
#define SW_PIN 17
#define SW_PULLUP_PIN 18

// LED pin declarations
#define LED1_PIN 18
#define LED2_PIN 23
#define LED3_PIN 24

// Switch pin declarations
#define SW_PIN 17
#define SW_PULLUP_PIN 18

// I2C device address
#define I2C_DEVICE_ADDRESS 0x68

// I2C bus speed
#define I2C_SPEED 100000

// I2C bus frequency
#define I2C_FREQUENCY 100000

// I2C bus mode
#define I2C_MODE 0

// I2C bus timeout
#define I2C_TIMEOUT 1000

// I2C bus retries
#define I2C_RETRIES 10

// I2C bus debug
#define I2C_DEBUG 0

// I2C bus verbose
#define I2C_VERBOSE 0

// I2C bus error
#define I2C_ERROR 0

// I2C bus success
#define I2C_SUCCESS 1

// I2C bus timeout
#define I2C_TIMEOUT_ERROR 2

// I2C bus retries
#define I2C_RETRIES_ERROR 3

// I2C bus debug
#define I2C_DEBUG_ERROR 4

// I2C bus verbose
#define I2C_VERBOSE_ERROR 5

// I2C bus error
#define I2C_ERROR_ERROR 6

// I2C bus success
#define I2C_SUCCESS_ERROR 7

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR 8

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR 9

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR 10

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR 11

// I2C bus error
#define I2C_ERROR_ERROR 12

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR 13

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR 14

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR 15

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR 16

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR 17

// I2C bus error
#define I2C_ERROR_ERROR_ERROR 18

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR 19

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR 20

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR 21

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR 22

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR 23

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR 24

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR 25

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR 26

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR 27

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR 28

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR 29

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR 30

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR 31

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 32

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 33

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 34

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 35

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 36

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 37

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 38

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 39

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 40

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 41

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 42

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 43

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 44

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 45

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 46

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 47

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 48

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 49

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 50

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 51

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 52

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 53

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 54

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 55

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 56

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 57

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 58

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 59

// I2C bus error
#define I2C_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 60

// I2C bus success
#define I2C_SUCCESS_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 61

// I2C bus timeout
#define I2C_TIMEOUT_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 62

// I2C bus retries
#define I2C_RETRIES_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 63

// I2C bus debug
#define I2C_DEBUG_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 64

// I2C bus verbose
#define I2C_VERBOSE_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR_ERROR 65
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# Why implement the solution?

- Implement to fix fundamental design
  - Stays within budget
  - Make a more green design
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# Clients and Users?

- People who are engineering enthusiasts
  - Artists
  - University Students
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